



Attorney's Docket No. : 7977/204002/US3480D1

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10.17.01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : H. Ohtani

Art Unit :

2815 TECHNOLOGY CENTER

Serial No.: 09/389,393 /

Examiner :

Baumeister, B. 2800

Filed : September 3, 1999

Title : SEMICONDUCTOR DEVICE HAVING INSULATED GATE ELECTRODE

Commissioner for Patents

Washington, D.C. 20231

AMENDMENT

Sir:

In response to the Official Action dated July 5, 2001,
Paper No. 12, in the above-referenced application, kindly amend
the above-identified application as follows.

In the Claims:

Please amend claims 11, 15, 16, 20, 21, and 25 as follows.

SUB F3 > 11. (Amended) A semiconductor device comprising:

a semiconductor layer;

a gate insulating film formed on said semiconductor

layer;

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

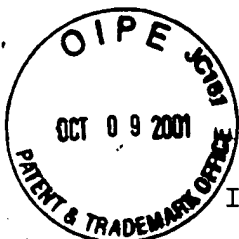
October 4, 2001

Date of Deposit

Signature

Susan Regan

Typed or Printed Name of Person Signing Certificate



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TRANSMITTAL LETTER

Correspondence relating to this application is enclosed.
The required fees are computed below. Please apply any charges
not covered, or any credits, to Deposit Account No. 06-1050.


Total Claims	30	-	30	=	0	\$0
Independent	6	-	6	=	0	\$0

TOTAL FEE DUE \$0

No fee is required.

Respectfully submitted,

Date: 10/4/01



Scott C. Harris
Reg. No. 32,030

Fish & Richardson P.C.
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San Diego, California 92122
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